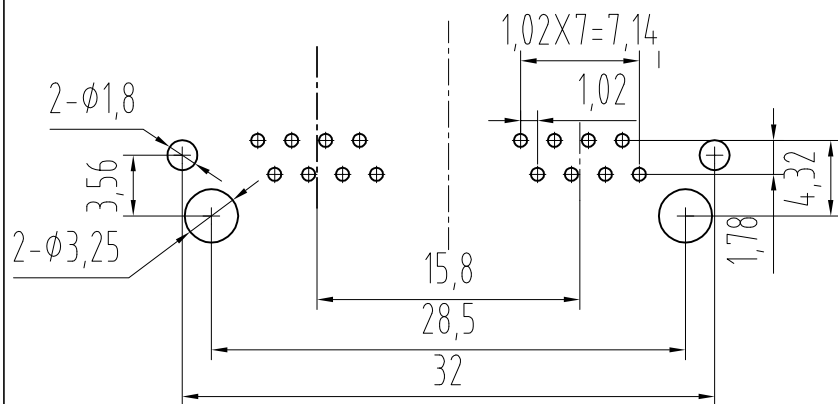
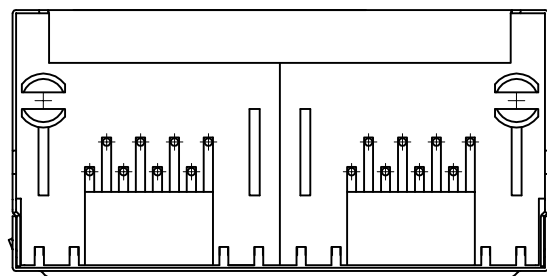
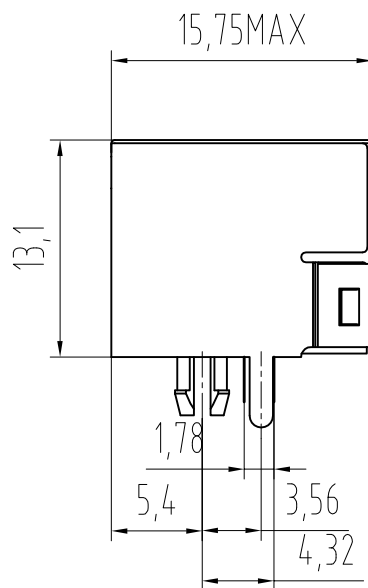
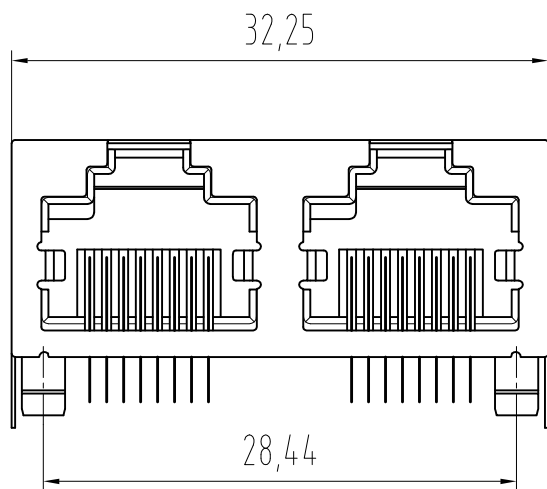


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



PC Board Layout
Component Side Shown

NOTES:

MATERIAL :

- 1.HOUSING MATERIAL :GLASS FILLED POLYESTER UL94V-0.
- 2.CONTACT MATERIAL :PHOSPHOR BRONZE ϕ 0.46mm.
- 3.PLATING :GOLD PLATING OVER NICKEL.
- 4.SHIELD :0.20mm THICKNESS COPPER ALLOY WITH NICKEL PLATED.
- 5.LED :OPTION

ELECTRICAL:

- 1.VOLTAGE RATING :125 VAC RMS.
- 2.CURRENT RATING :1.5 AMP.
- 3.CONTACT RESISTANCE :30 MILLIOHMS MAX.
- 4.INSULATION RESISTANCE :500 MEGOHMS MIN @ 500V DC .
- 5.DIELECTRIC WITHSTANDING RESISTANCE :1000V AC RMS 50Hz. 1MIN.


MECHANICAL:

- 1.DURRABILITY :750 CYCLES MIN.
- 2.PCB RETENTION PRE-SOLDER :1 LB MIN.

ENVIRONMENTAL:

- 1. STORAGE : -40 $^{\circ}$ ~+85 $^{\circ}$
- 2. OPERATION : 0? TO 70?.

PART NUMBER:

RELATED LISTS	MM (INCH)	DFTD SHB	DATE 2008.03.05	 TITLE 5JA SERIES 1XZ LED JACK SHIELDED		
	TOLERANCES EXCEPT AS NOTED	CHKD LANBO	DATE 2008.03.05			
	MM .0 ± 0.15 .00 ± 0.075 .000 ± 0.05	NFO	DATE			
	ANGLES ± 0.5	APPVL	DATE			
THIRD ANGLE PROJECTION	MATERIAL :	Q.T.Y :	FINISH :	DRAWING NO. TESJA1007	SIZE A3	REV 0
		SCALE :		/PART NO.	DO NOT SCALE DRAWING	
					SHEET X	OF Y